Electronic Patent Application Fee Transmittal							
Application Number:	10	10797972					
Filing Date:	10-Mar-2004						
Title of Invention:	SE LII	FABRICATION OF CONDUCTIVE LINES INTERCONNECTING FIRST CONDUCTIVE GATES IN NONVOLATILE MEMORIES HAVING SECOND CONDUCTIVE GATES PROVIDED BY CONDUCTIVE GATE LINES, WHEREIN THE ADJACENT CONDUCTIVE GATE LINES FOR THE ADJACENT COLUMNS ARE SPACED FROM EACH OTHER, AND NON-					
First Named Inventor/Applicant Name:	Yi Ding						
Filer:	Michael Shenker/Jeannie Ha						
Attorney Docket Number:	M-15297 US						
Filed as Large Entity							
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